

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Inventor Application of :

Alexandre Andreev  
Igor Vikhliantsev  
Ranko Scepanovic

09/883,733

Filed :

June 18, 2001

For :

Psuedo-Random One-to-One  
Circuit Synthesis

Group Art Unit : 2132

Examiner : Lemma, Samson

Atty Docket : 1496.00129 / 01-308

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below:

Manu Kashyap

1/12/06  
Date

*[Signature]*  
Signature

**SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85**

## Official Draftsman

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
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408-433-7475

Respectfully submitted,

*[Signature]*

Timothy Croll

Reg. No. 36,771

Date: 12 JAN 06